

MoDES PROJECT MEETING

Sønderborg, 12 – 13 March 2007

Day 1

	Welcome and Practical Matters	11:00 – 11:15
Kim Larsen	Application of Timed Games	11:15 – 12:00
	Lunch	12:00 - 13:00
Aske Brekling	A Timed-Automata Model for Verifying Multiprocessor System-on-Chip	13:00 – 13:30
Jens Ellebæk and Kristian Knudsen	MOVES – A Tool for Modelling and Verification of Embedded Systems	13:30 – 14:00
Michael Hansen	Towards Cost Analysis Using Interval Logic	14:00 – 14:30
Luo Yigui	Transform DTTPN to Time Automaton	14:30 – 15:00
	Coffee break	15:00 – 15:30
Nicolae Marian	Minimization of the Verification Effort	15:30 – 16:00
Yu Guo	A Structured Production Cell Verification Model in Uppaal	16:00 – 16:30
Xin Wang	PAJ Case Study: COMDES Specification of the Whispa Medical Ventilation System	16:30 – 17:00
Jacob Rasmussen	Controller Synthesis of Climate Controllers for Stables	17:00 – 17:30
	Discussion	17:30 – 18:30
	MoDES Steering Group meeting	17:30 – 18:30
	Dinner	19:30 -

Day 2

Anders Ravn	Activities, Roles and Tools in Component-Based Development	9:00 – 9:30
Istvan Knoll	Tools for Embedded System Development - a Survey	9:30 - 10:00
Stephan Korsholm	Java in Embedded Applications	10:00 – 10:20
	Coffee break	10:20 – 10:50
Marius Mikucionis	Testing Real-Time Systems Online	10:50 – 11:30
Shuhao Li	Live Sequence Charts as Test Purposes for Real-Time Systems	11:30 – 11:50
Saulius Pusinskas	Live Sequence Charts in Testing	11:50 – 12:10
	Lunch	12:10 – 13:10
Martin Riisgaard-Jensen	SKOV Case Study	13:10 – 13:40
Claus Thybo	Danfoss Case Study	13:40 – 14:10
	Coffee break	14:10 – 14:30
	Discussion	14:30 – 15:00